

Academic Year: 2017-18 Even Semester

S. No.	Name of the Coordinator(s) With Designation	Name of Programme & Sponsoring Agency (if any)	No. of Participants Attended (External/Internal)	Date(s)
1.	Dr. R. Sudhakar, Professor & Head, Dept of ECE	National Conference on communication and Instrumentation Systems (NCCIS)	External:29 Internal: 11	20.4.2018
	Dr. K. Vijeya Kumar, HOD/EIE			
2.	Dr. R. Sudhakar, Professor & Head, Dept of ECE	Two days workshop on Network simulators (ns3) and cloud networks	External:- Internal: 46	15.02.2018 & 16.02.2018
	Dr. K. Sumathi Associate Professor			
3.	Dr. R. Sudhakar, Professor & Head, Dept of ECE	One day workshop on Custom IC Design, CMOS Process Integration, IC Fabrication, Packaging and Testing	External:- Internal: 80	23.02.2018
	Dr. K. N. Vijeya Kumar, Associate Professor, ECE			

Academic Year: 2017-18 Odd Semester

S.No.	Name of the Coordinator(s) With Designation	Name of Programme & Sponsoring Agency (if any)	No. of Participants Attended (External/Internal)	Date(s)
4.	Dr. R. Sudhakar, Professor & Head, Dept of ECE	Two Days workshop on IOT and Data Analytics	External:28 Internal:8	08.09.2017 & 09.09.2017
	Dr. K. Mohaideen Abdul Kadhar Assistant Professor (SG)			

5.	Dr. K. N. Vijeya Kumar, Associate Professor, ECE	Two days workshop on CMOS RF receiver front end design using Cadence and ADS EDA Tools	External:10 Internal:8	26.08.2017 & 27.08.2017
	Dr. K. Sumathi Associate Professor			
6.	Dr. K. Sumathi Associate Professor	Two days workshop on Advanced Digital Signal Processing with Matlab	External:7 Internal: 7	18.09.2017 & 19.09.2017
7.	Dr. K. N. Vijeya Kumar, Associate Professor, ECE	Two days workshop on Implementation of Image Processing Algorithm on FPGA	External:- Internal:18	27.07.2017 & 28.07.2017
8.	Dr. K. N. Vijeya Kumar, Associate Professor, ECE	Five days workshop on System design and verification using System Verilog HDL	External:- Internal: 30	03.07.2017 to 07.07.2017